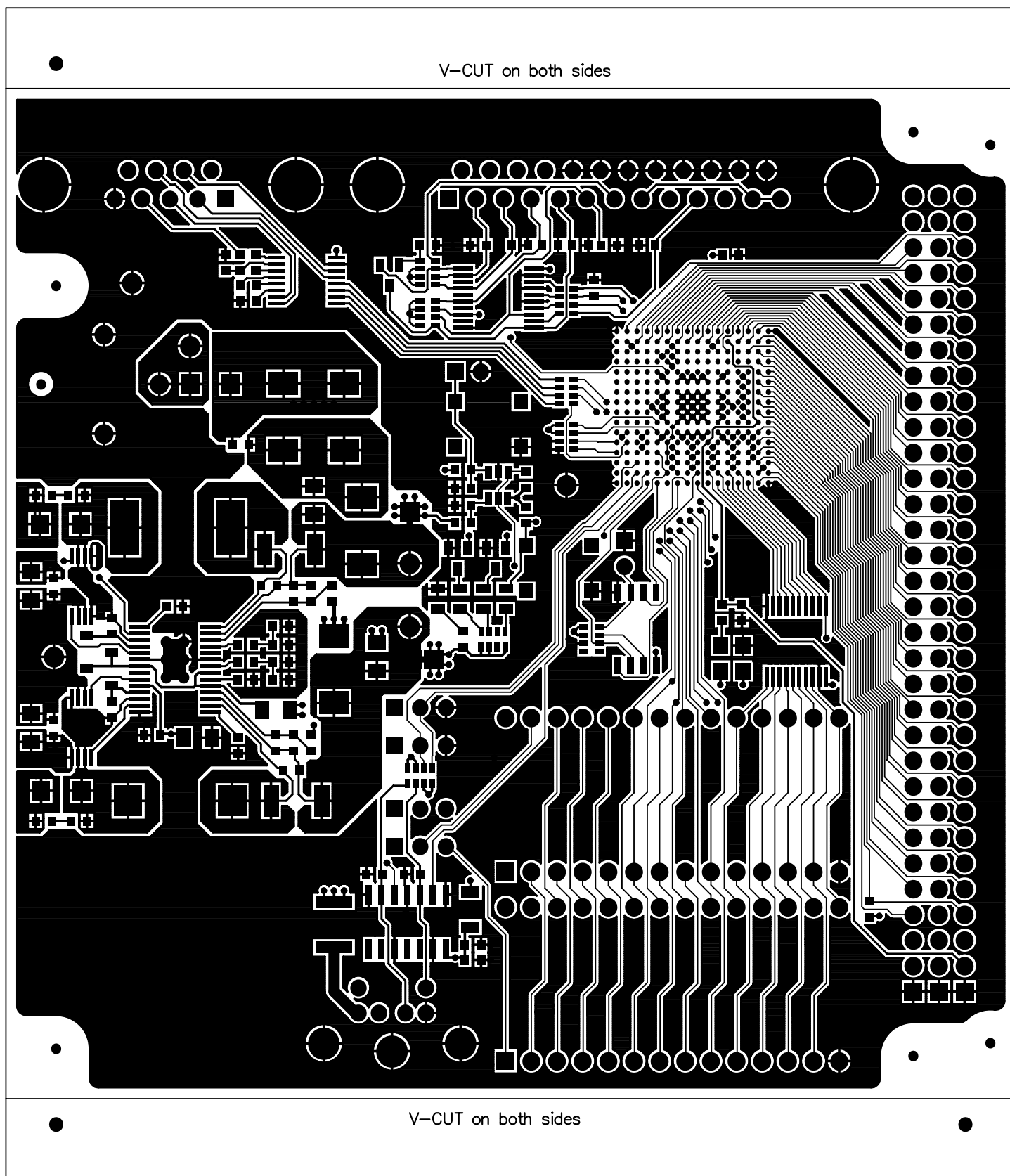


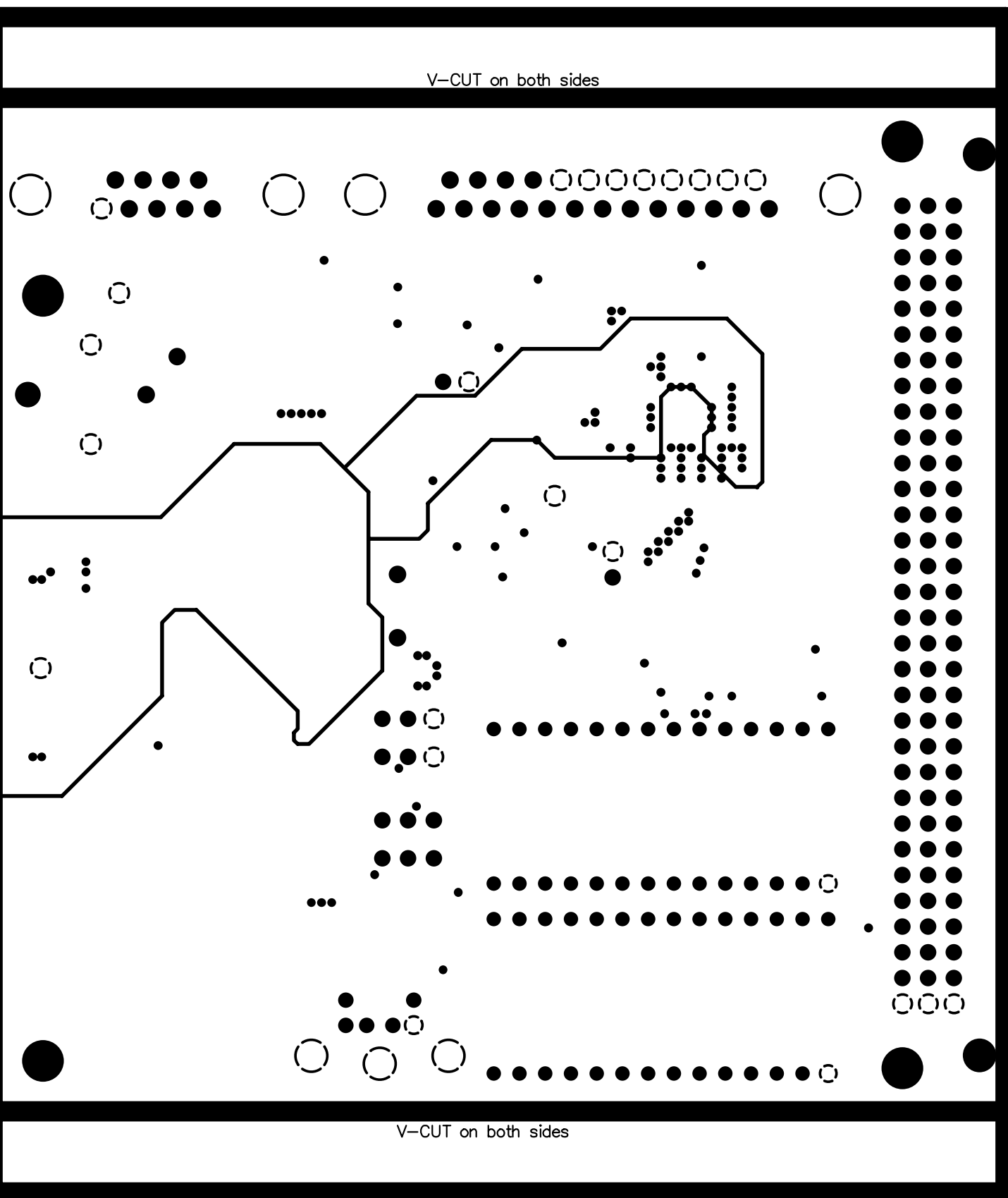
aq018\_diycalculator\_fpga.PCBDOC

Top Layer .GTL



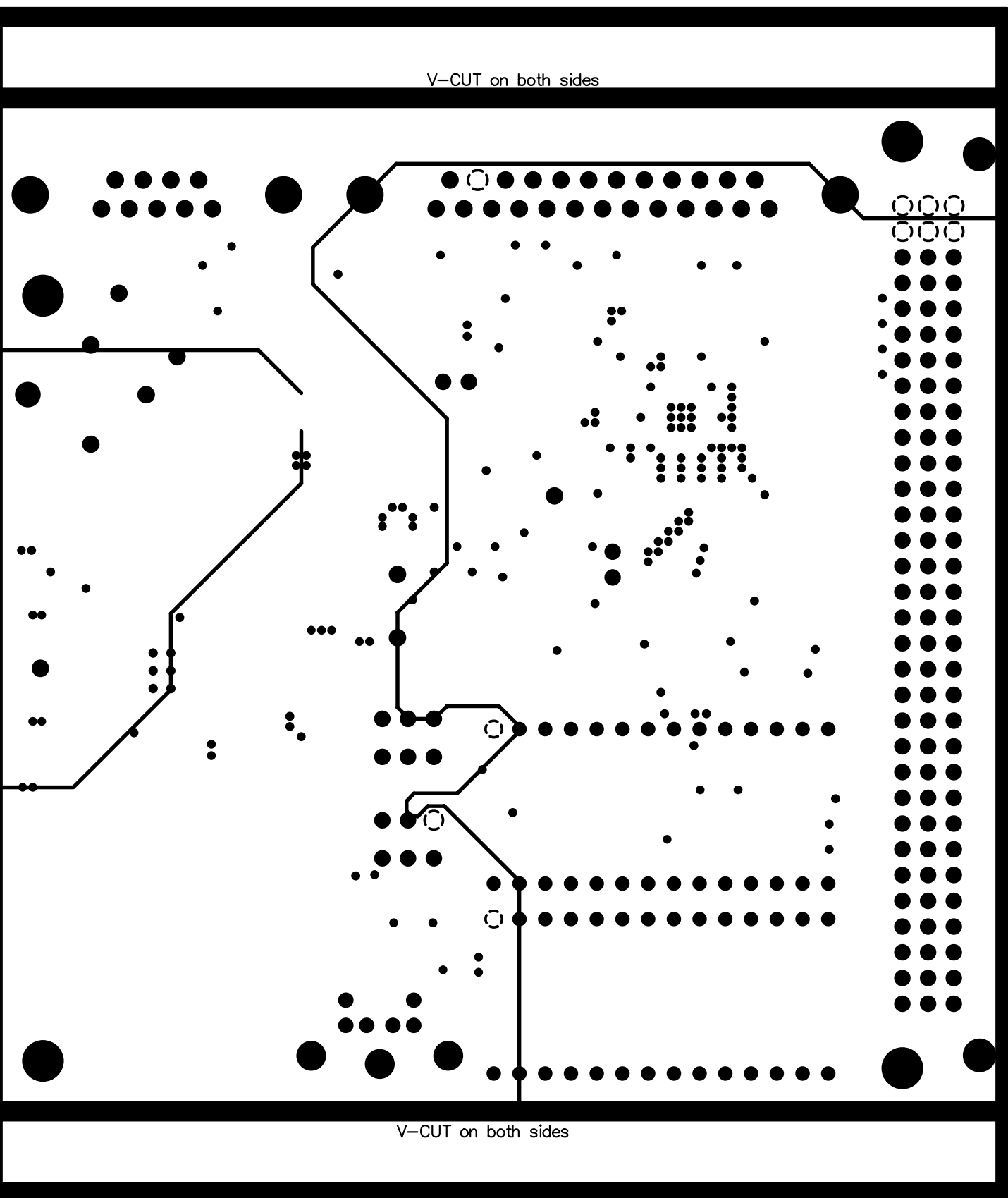
aq018\_diycalculator\_fpga.PCBDOC

Internal Plane1 .GP1



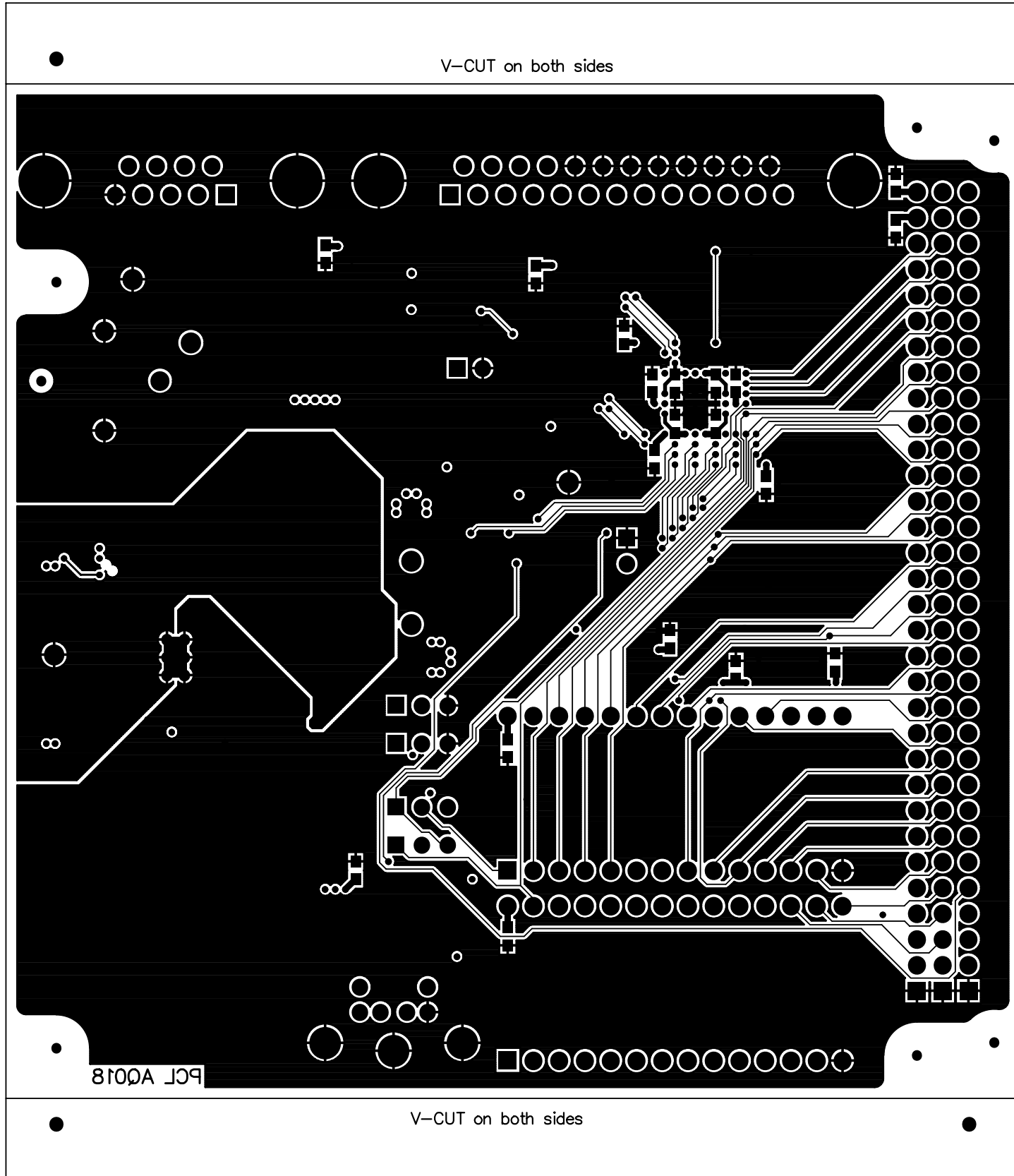
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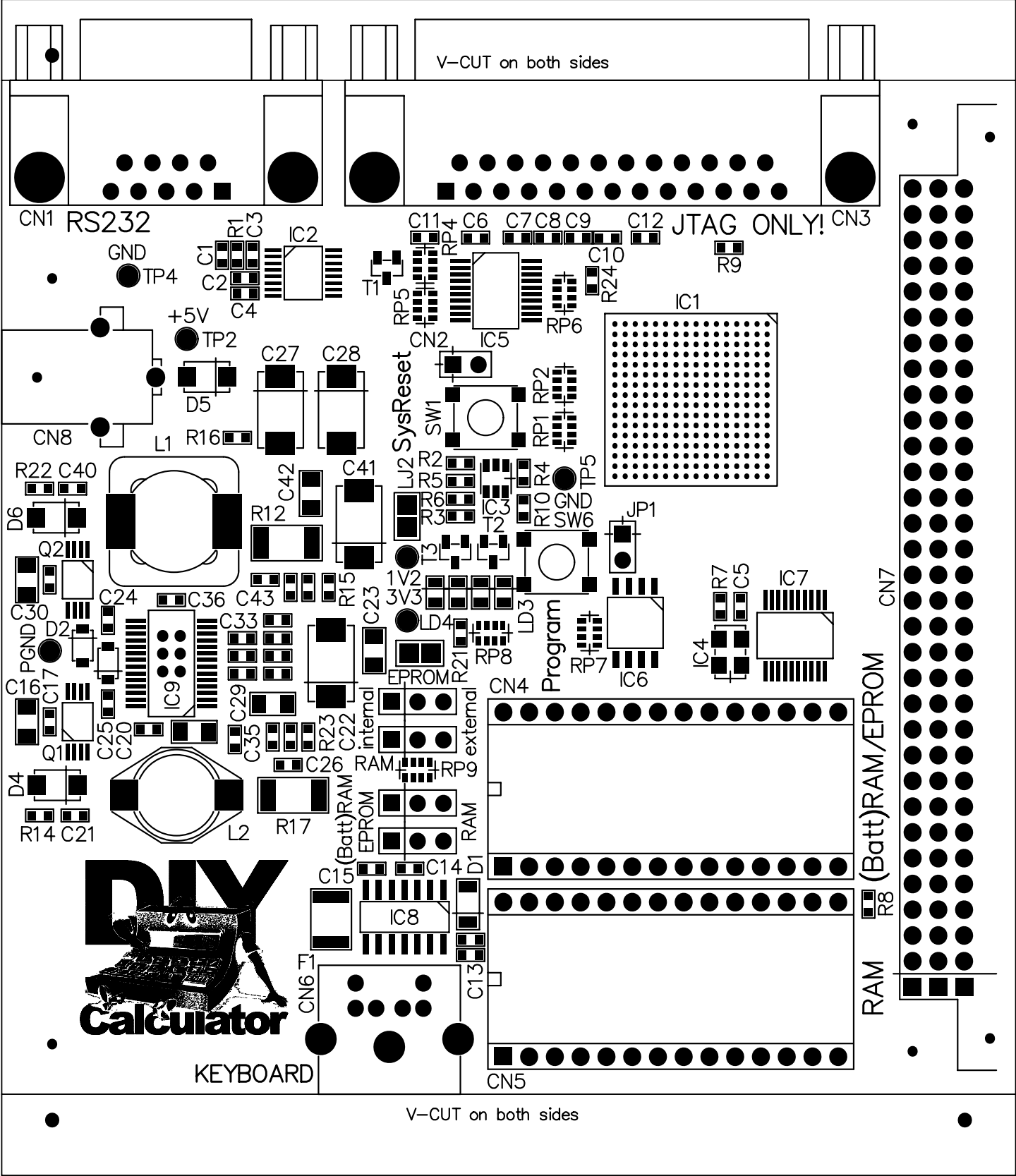
Internal Plane2 .GP2

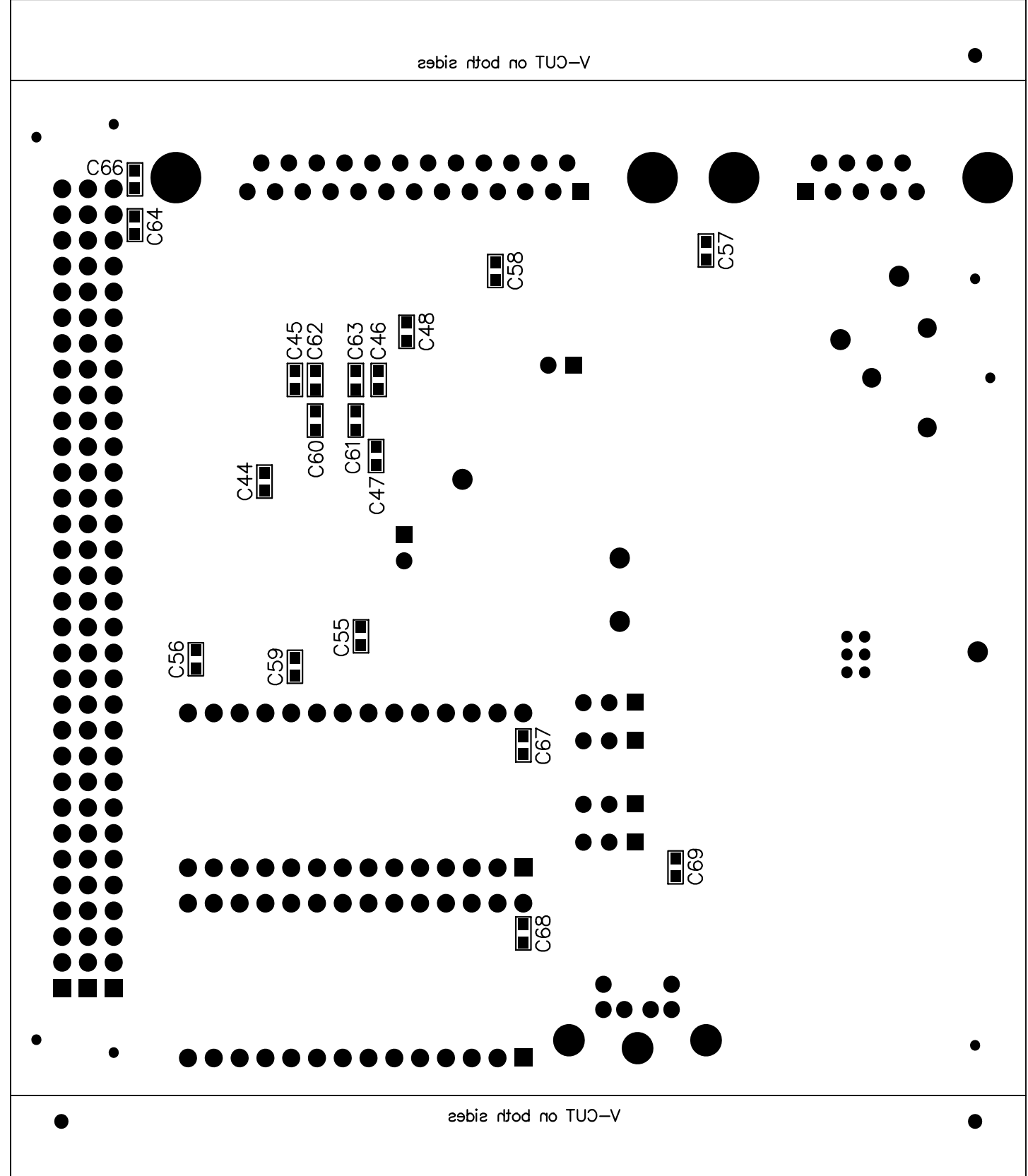


aq018\_diycalculator\_fpga.PCBDOC

Bottom Layer .GBL



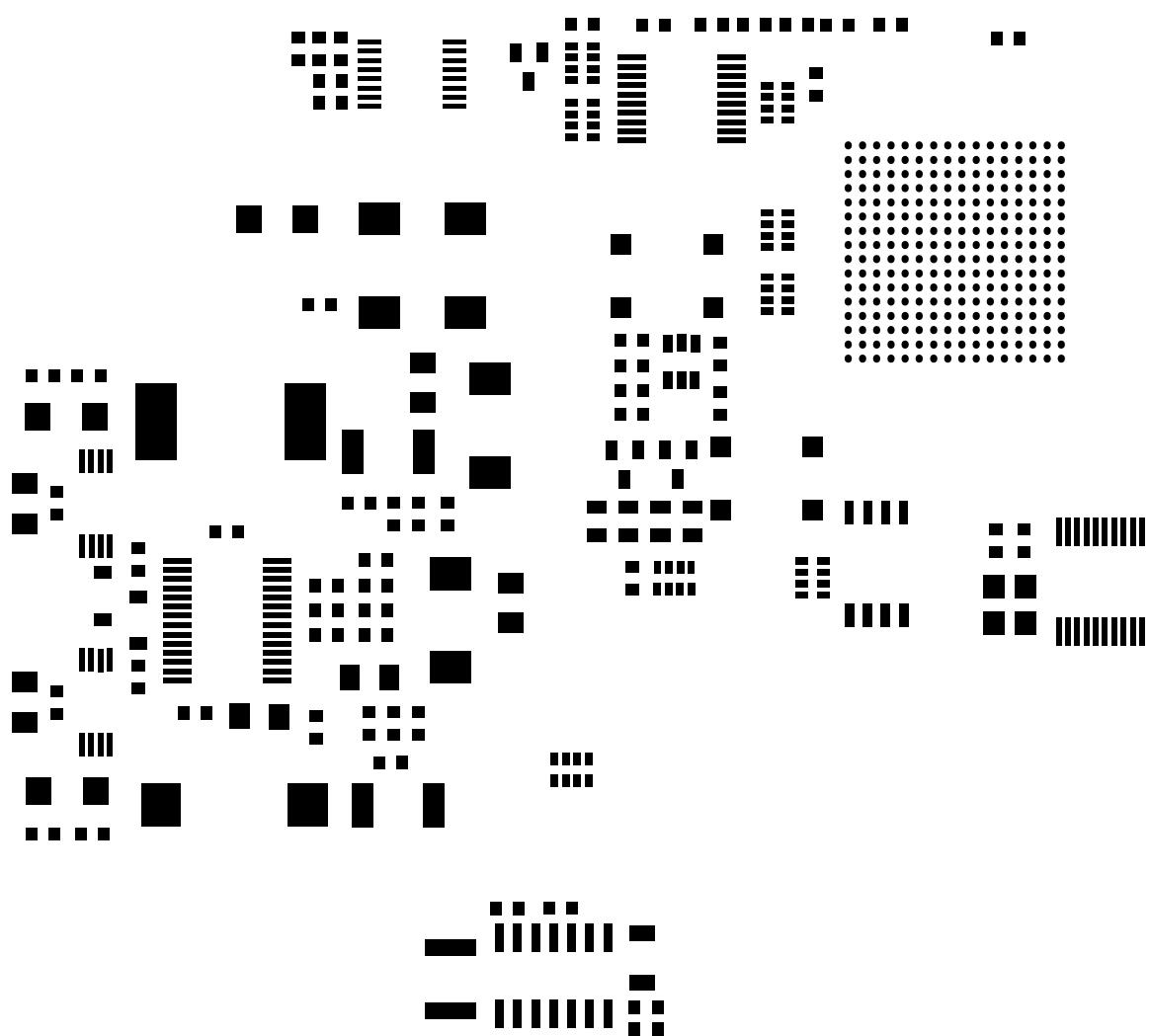




aq018\_diycalculator\_fpga.PCBDOC

Top Paste .GTP

V-CUT on both sides

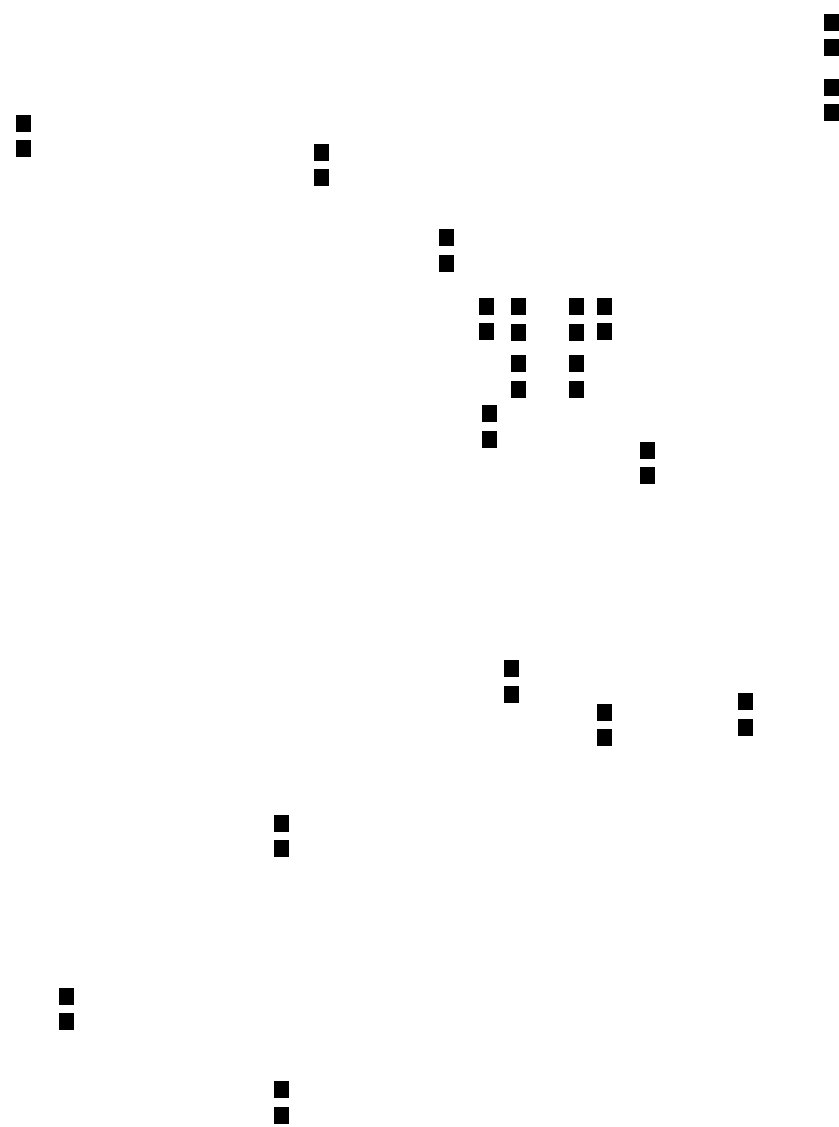


V-CUT on both sides

aq018\_diycalculator\_fpga.PCBDOC

Bottom Paste .GBP

V-CUT on both sides

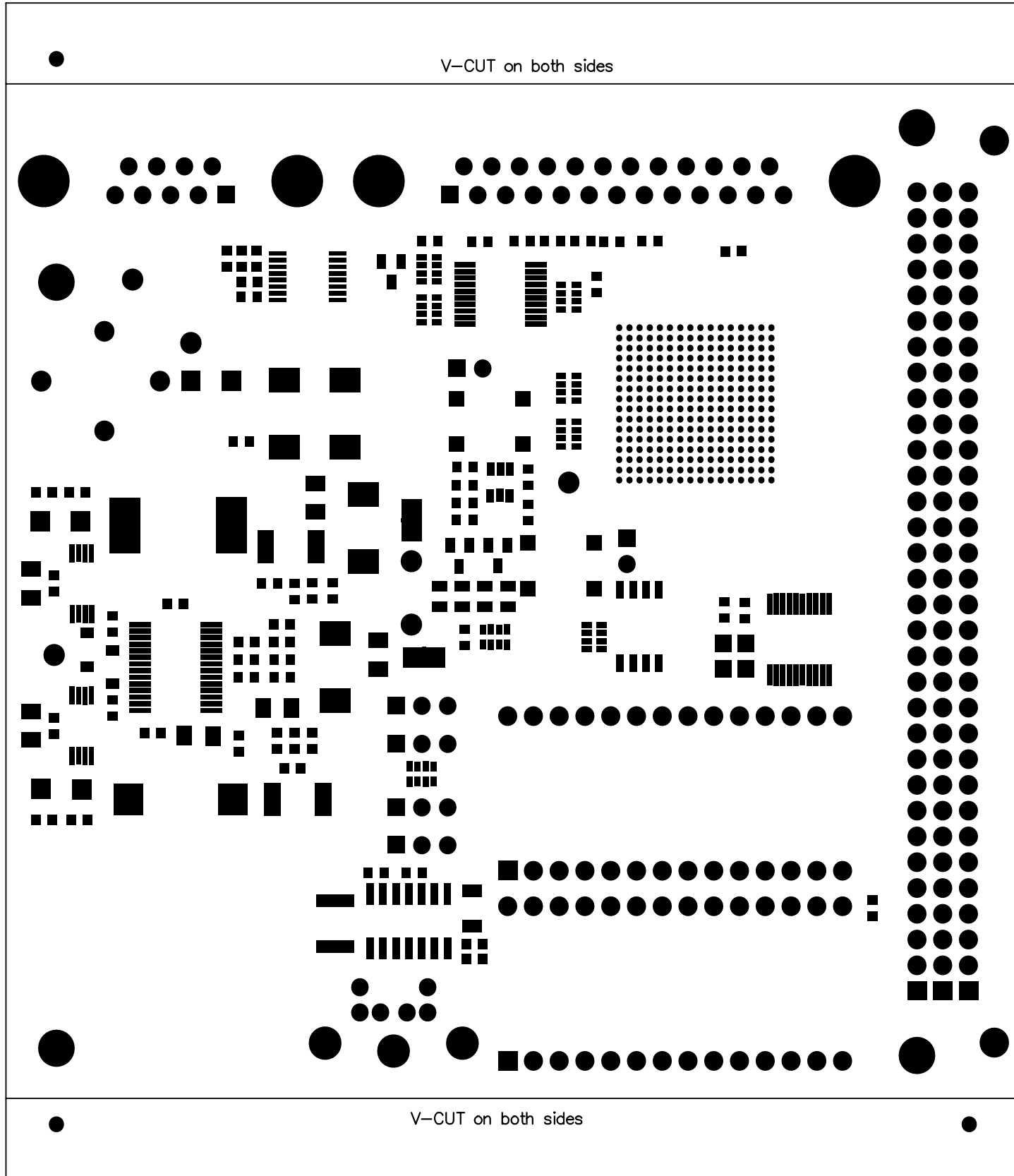


V-CUT on both sides



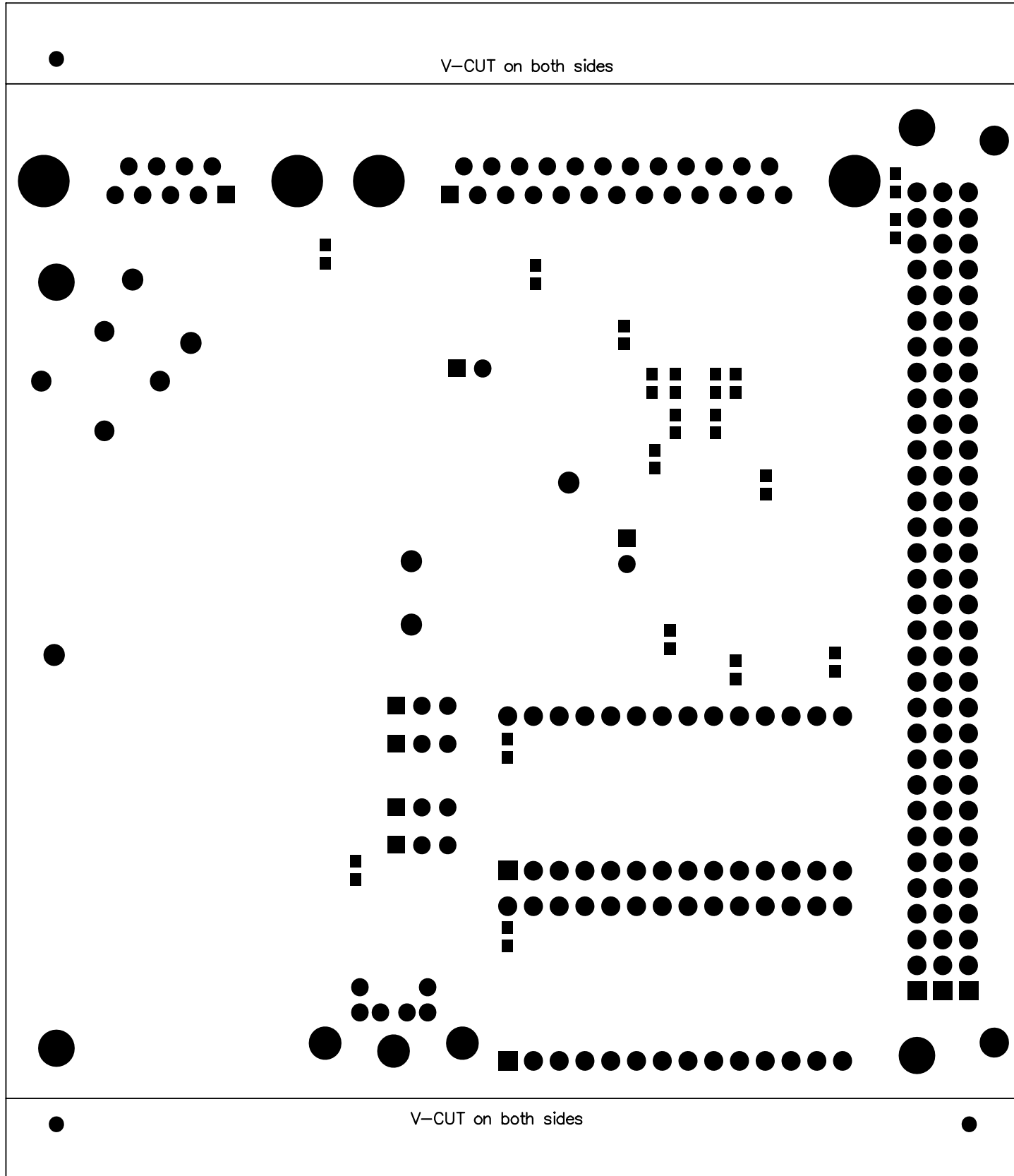
aq018\_diycalculator\_fpga.PCBDOC

Top Solder .GTS



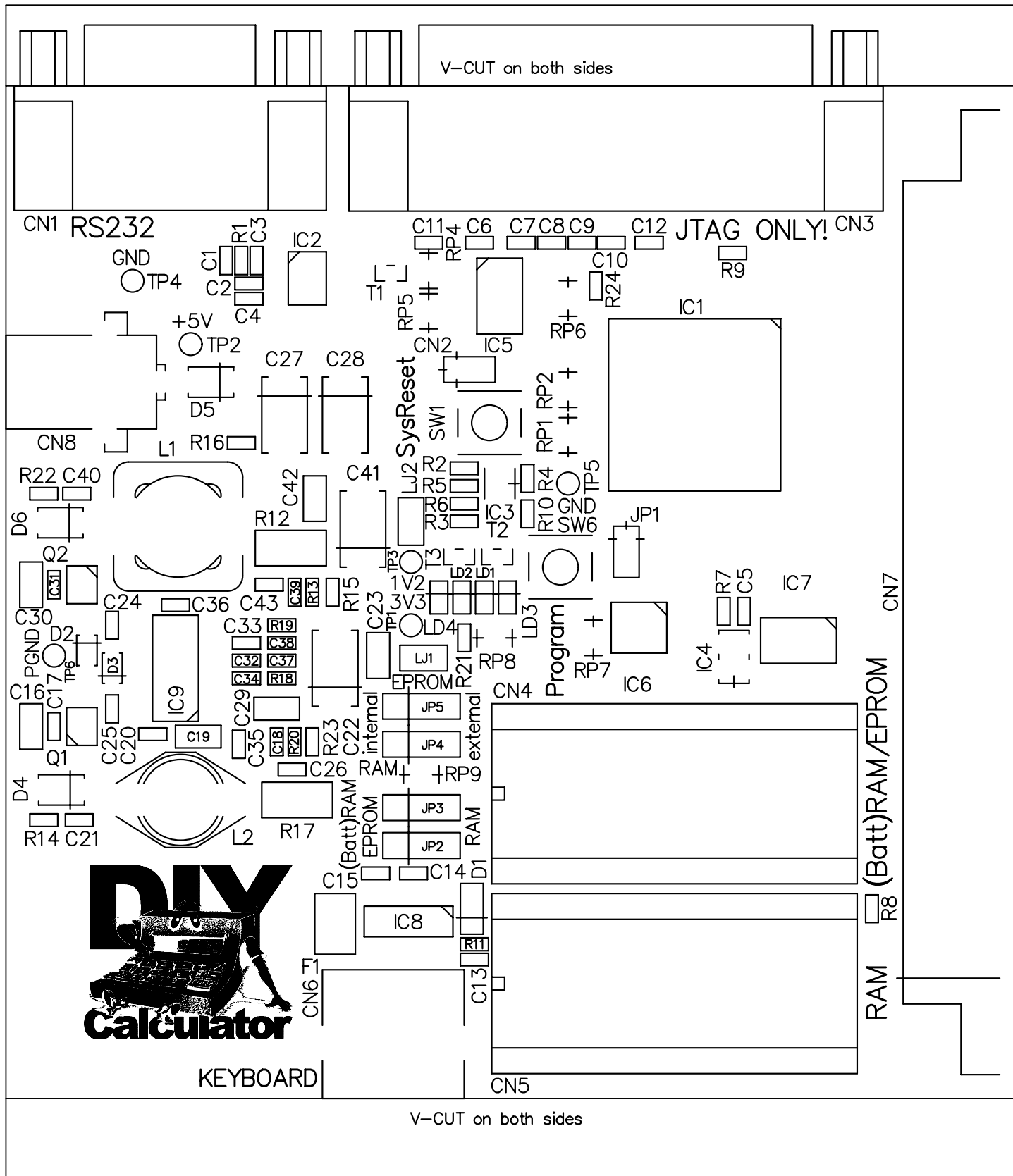
aq018\_diycalculator\_fpga.PCBDOC

Bottom Solder .GBS



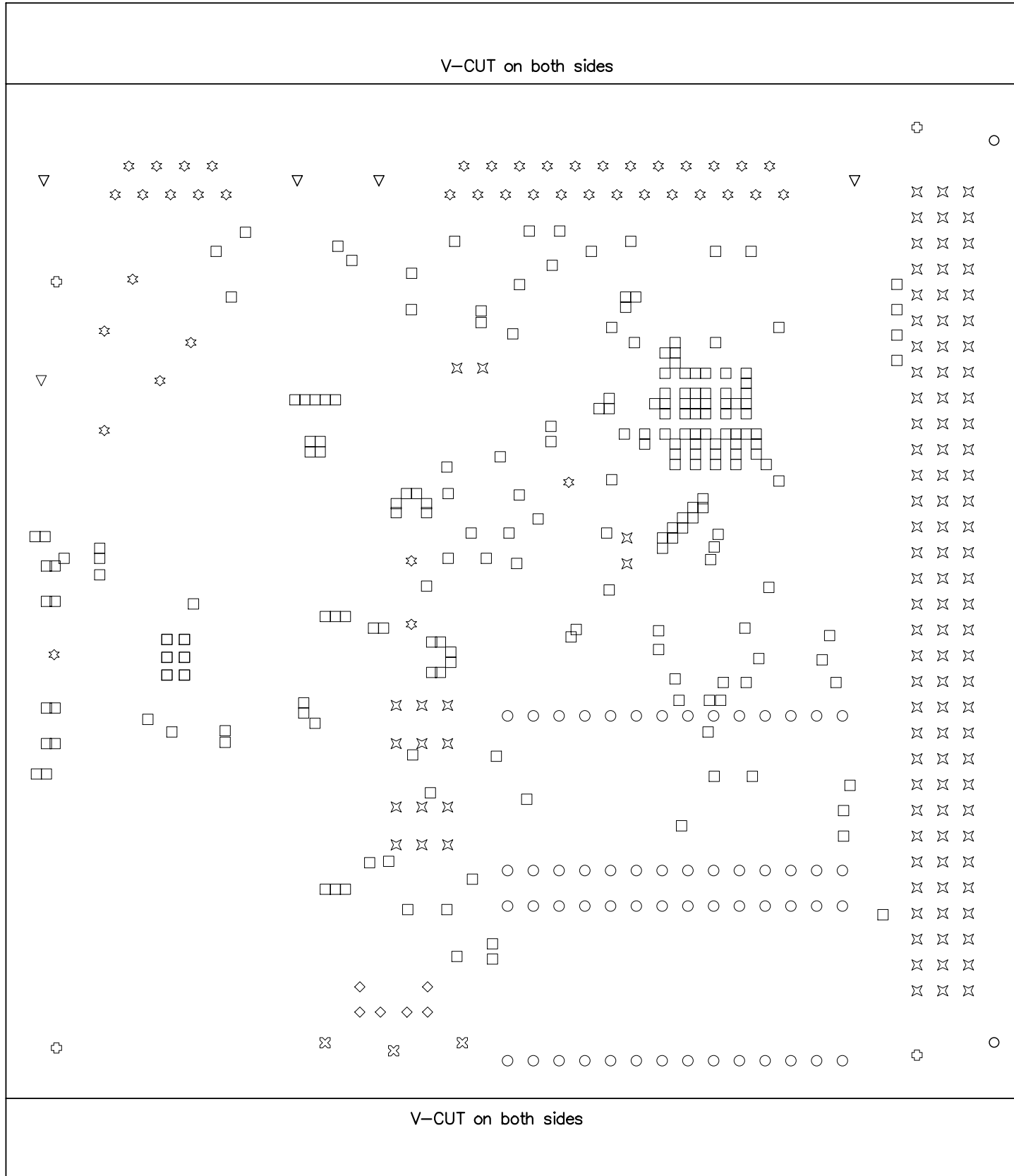
# aq018\_diycalculator\_fpga.PCBDOC

## Top Overlay .GTO



# aq018\_diycalculator\_fpga.PCBDOC

## Drill Drawing .DRL



Layer Stack Up Detail for: aq018\_diycalculator\_fpga.PCBDOC

Layer Name
Top Layer
InternalPlane1
InternalPlane2
Bottom Layer

FR4 / 1.6mm / 35u / Surface Sn

□	209	9.842mil	0.25mm	PTH	Round
□	6	11.811mil	0.3mm	PTH	Round
○	56	31.496mil	0.8mm	PTH	Round
◇	6	35.433mil	0.9mm	PTH	Round
⊗	112	39.37mil	1mm	PTH	Round
☆	43	43.307mil	1.1mm	PTH	Round
▽	1	74.803mil	1.9mm	NPTH	Round
⊗	3	90.551mil	2.3mm	PTH	Round
○	2	106.299mil	2.7mm	NPTH	Round
▽	4	120.079mil	3.05mm	PTH	Round
⊕	4	137.795mil	3.5mm	NPTH	Round
	446	Total			

### Drilling Details